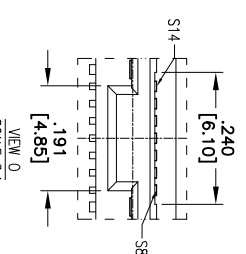
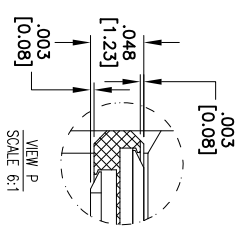
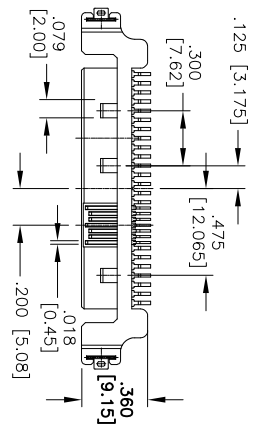
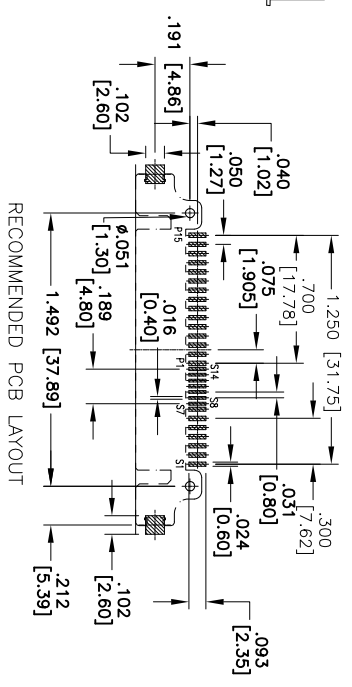
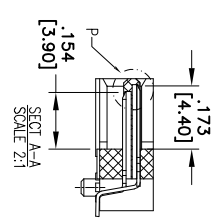
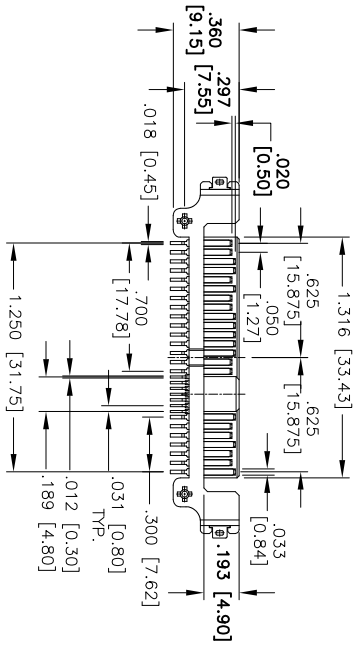
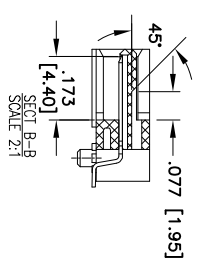
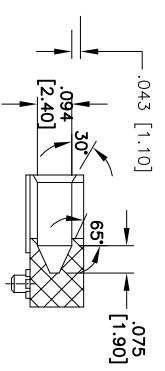
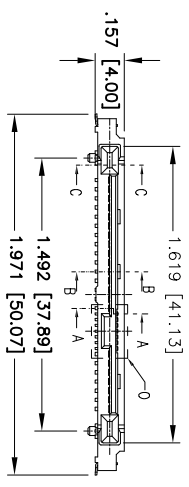


Rev	AWO #	Description	Date	Appr
-		RELEASED	1/9/12	

APPROVED: \_\_\_\_\_  
 DATE: \_\_\_\_\_ TITLE: \_\_\_\_\_



Signal Segment	Pin NO.	Type	Power Segment	Pin NO.	Type
S1	GND	S1	GND	P1	V33
S2	A+	P2	V33	P2	V33
S3	A+	P3	V33	P3	V33
S4	GND	P4	GND	P4	GND
S5	B+	P5	GND	P5	GND
S6	B+	P6	GND	P6	GND
S7	GND	P7	V3	P7	V3
S8	GND	P8	V3	P8	V3
S9	A+	P9	GND	P9	GND
S10	A+	P10	GND	P10	GND
S11	GND	P11	DSS	P11	DSS
S12	B+	P12	GND	P12	GND
S13	B+	P13	GND	P13	GND
S14	GND	P14	V12	P14	V12
S14	GND	P15	V12	P15	V12



RECOMMENDED PCB LAYOUT

A

B

C

D

A

B

C

D

A

B

C

D

A

B

C

D

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